

**SIS Mixer/Preamp Numbering Scheme  
Version 1.01 2001-11-26**

Description	Abbreviations	Example
<b>Wafer</b>		
Wafer #		<b>UVaV-L811A</b>
Section		<b>1</b>
Cell		<b>D5</b>
Row		<b>2</b>
Chip		<b>D2</b>
Mixer Block		<b>2</b>
<b>Type of Mixer</b>		<b>BS</b>
Sideband Separating	SS	
Balanced	BB	
Balanced, Sideband Separating	BS	
Building Block	(none)	
<b>Type of Chip</b>		<b>I</b>
Individual	I	
Multiple	M	
<b>IF Output Band</b>		<b>A</b>
ALMA	A	
L-band	L	
Uncommitted	U	
<b>RF Frequency Band</b>		<b>37</b>
200-300 GHz	37	
<b>Revision Number</b>		<b>1</b>
e.g. 1 <sup>st</sup> revision	1	
<b>Preamp Numbering</b>		<b>IF4-12P.01</b>
IF4-12	1	
Prototype, SN 1	P.01	

**Examples:**

To accommodate mixer assemblies containing multiple sub mixers and multiple preamps, both wafer and preamp descriptions will be placed after the mixer description:

- 1) Balanced, sideband separating mixer using single chip, ALMA IF, and two preamps:

BSIA371-2-UVaV-L811A-1-D5-2-D2-IF4-12P.01/02

- 2) Balanced, sideband separating mixer using 4 chips, ALMA IF, and two preamps:

BSMA371-2  
UVaV-L811A-1-D5-2-D2  
UVaV-L811A-1-D5-2-D3  
UVaV-L811A-1-D5-2-D4  
UVaV-L811A-1-D5-2-D5  
IF4-12P.01/02