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Subject:Allan Deviation of Phase Jumps

Jumps or pops in phase existed in about 30% of the CTS phase stability measurements for Cartridge B6-002. Although those phase jumps have been eliminated by rerouting cables, analysis of how they affect phase is useful. This note shows Allan deviation of phase:

- 1. after removal of phase jumps,
- 2. comparing results using CTS and my software, and
- 3. after removal of both phase jumps and  $2^{nd}$  order phase drift.

Allan deviation of phase was calculated using both my software<sup>1</sup> and the CTS software, which originated from Jason Castro in Greenbank and employs a slightly different algorithm<sup>2</sup>.

<u>Figure 1</u> shows phase with a jump for LO = 221 GHz, Pol 1, LSB. <u>Figure 2</u> is phase after the jump is removed by adding a 7° offset during the interval of the jump. <u>Figure 3</u> is phase after removing the  $2^{nd}$  order drift from the jump-corrected phase in <u>Figure 2</u>. Allan deviation for these cases is graphed in <u>Figure 4</u> using both the current CTS software and my software. Note how removing the  $2^{nd}$  order drift component improves by a factor of 3 or 4 Allan deviation for long averaging times.

<u>Figure 5</u> and <u>Figure 6</u> show uncorrected and fixed phase jump data for LO = 221 GHz, Pol 0 USB and <u>Figure 7</u> shows a significant improvement in Allan deviation.

<u>Figure 8</u> and <u>Figure 9</u> are graphs of phase for a case where the variance in the Allan deviation is small. Also note that for this case, the discrepancy is small between the different software versions.

The results show that eliminating phase jumps and  $2^{nd}$  order drift is important for reducing Allan deviation. Subsequent measurements prove that minimizing temperature swings in the existing test apparatus is essential to meet stability specifications. A test system less sensitive to temperature changes is under construction, but long lead times will delay using that system for several months.

<sup>&</sup>lt;sup>1</sup> Effland and Schmitt, "Gain Stability Measurements of Band 6 Cartridge SN002", NRAO memo dated 2005-12-08, (ALMA number FEND-40.02.06.00-168-A-REP), Appendix I, Page 11, available <u>here</u>.

<sup>&</sup>lt;sup>2</sup> Using a LabVIEW Virtual Instrument writing using the "Tao=Input (Interlaced)" method.

<sup>\\</sup>Cvfiler.cv.nrao.edu\cv-cdl-sis\CartTstData\Cart6.002productionTSTdata\phaseSTAB\ShiftedData.doc Last Saved: Mon 2008-04-14 13:20:00



**Figure 1: Original Phase and Temperature Data** 

Figure 2: Phase and Temperature Data of Figure 1 with phase jumps fixed







Figure 4: Allan Deviation for original, shifted, and 2nd order corrected phase (Figure 1, Figure 2, and Figure 3)





Figure 5: Original Phase and Temperature Data, LO = 221 GHz, Pol 0 USB





Figure 7: Allan Deviation for phase of Figure 5 and Figure 6





Figure 8: Phase and Temperature, 245 GHz, Pol 1, USB



