

ALMA CHIP SERIAL PROGRAM WORD

<u>BIT</u> <u>NUMBERS</u>	<u>SIGNAL NAMES</u>	<u># bits</u>	<u>FIGURE</u>	<u>DESCRIPTION</u>
15 : 0	R0-M[15:0]	16	2, 4	program row 0 correlator multiplexers
31 : 16	R1-M[15:0]	16	2, 4	program row 1 correlator multiplexers
47 : 32	R2-M[15:0]	16	2, 4	program row 2 correlator multiplexers
63 : 48	R3-M[15:0]	16	2, 4	program row 3 correlator multiplexers
79 : 64	WRAP-BLK[15:0]	16	2, 4	In Blk x, select data from previous 256 lag block.
84 : 80	DLY[4:0]	5	3, 8	program delay lines in DB0 to DB15 and blanking
85 : 85	AUXENBL	1	3	Enable data activity out of the Aux Bus outputs.
86 : 86	LTOR	1	3	Program right side output bus drivers.
87 : 87	16BITCNTR	1	6	1 for 16 bit counter. 0 for faster test mode.
89 : 88	CENTERBUS[1:0]	2	3	program internal tri-state drivers
91 : 90	LEAD-CTRL	2	4	Determine blocks with extra LEAD delay.
92 : 92	R0FULLACC	1	6	row 0 21 bit/25 bit accumulator bit
93 : 93	R1FULLACC	1	6	row 1 21 bit/25 bit accumulator bit
94 : 94	R2FULLACC	1	6	row 2 21 bit/25 bit accumulator bit
95 : 95	R3FULLACC	1	6	row 3 21 bit/25 bit accumulator bit
96 : 96	DIAGONAL0AUTO	1	4	Diagonal block 0 in Auto mode, vs cross.
97 : 97	DIAGONAL5AUTO	1	4	Diagonal block 5 in Auto mode, vs cross.
98 : 98	DIAGONAL10AUTO	1	4	Diagonal block 10 in Auto mode, vs cross.
99 : 99	DIAGONAL15AUTO	1	4	Diagonal block 15 in Auto mode, vs cross.
100 : 100	R0OVERSAMP	1	5	row 0 over sample program bit
101 : 101	R1OVERSAMP	1	5	row 1 over sample program bit
102 : 102	R2OVERSAMP	1	5	row 2 over sample program bit
103 : 103	R3OVERSAMP	1	5	row 3 over sample program bit
TOTAL		104		

LSB of an array always assigned to the lowest bit numbers.